*FIG. 1(b)*

```

    graph LR
        MC((MICROPHONE MC)) --> AIF[AUDIO INTERFACE AIF]
        SPK((SPEAKER SPK)) --> AIF
        AIF <--> BBP[BASEBAND BBP]
        AIF <--> ASIC[ASIC]
        AIF <--> DSP[DSP]
        AIF <--> MPU[MICRO-PROCESSOR UNIT MPU]
        AIF <--> MR[MEMORY MR]
        MPU <--> CRL[LIQUID CRYSTAL DRIVER CRL]
        CRL <--> PNL[LIQUID CRYSTAL PANEL PNL]
        PNL <--> ANT[ANT]
    
```

FIG. 2

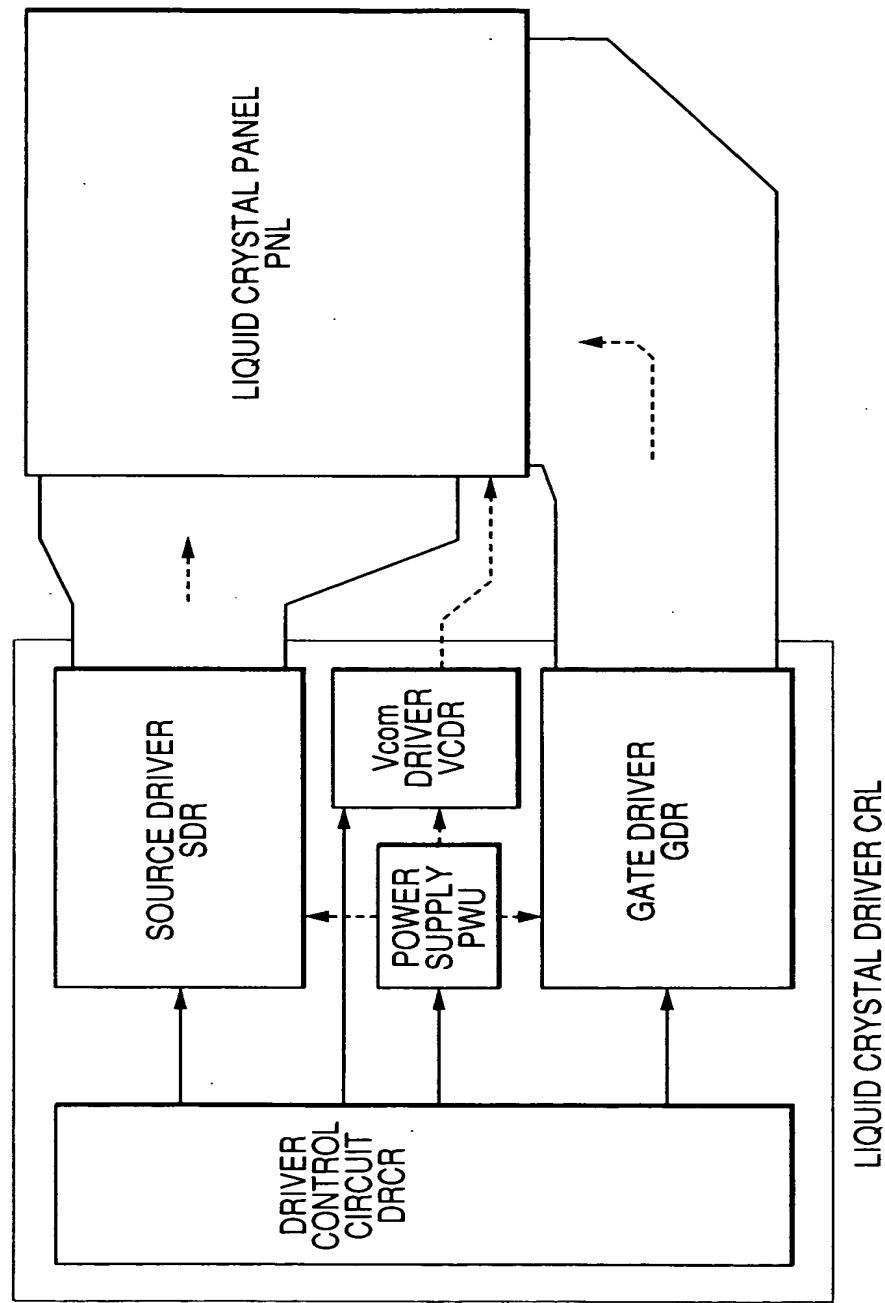


FIG. 3(a)

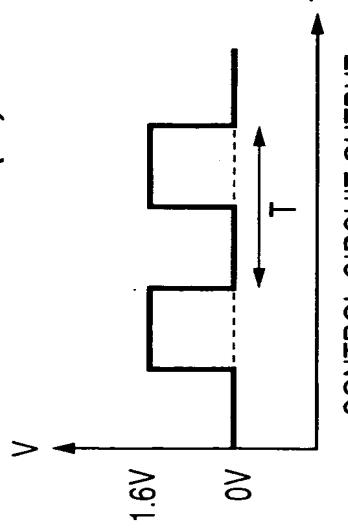


FIG. 3(b)

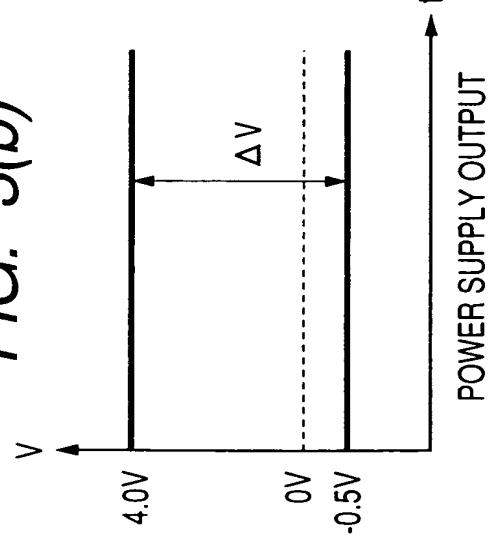


FIG. 3(c)

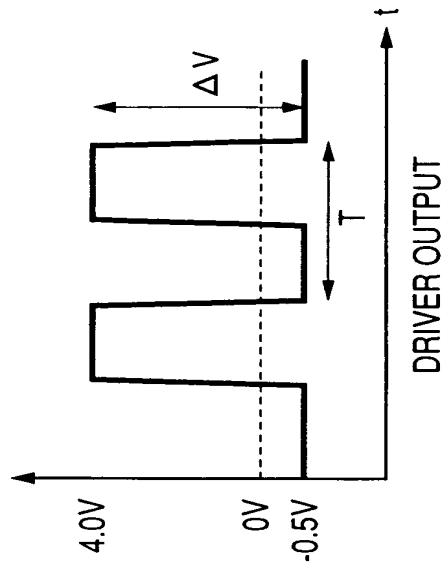


FIG. 4

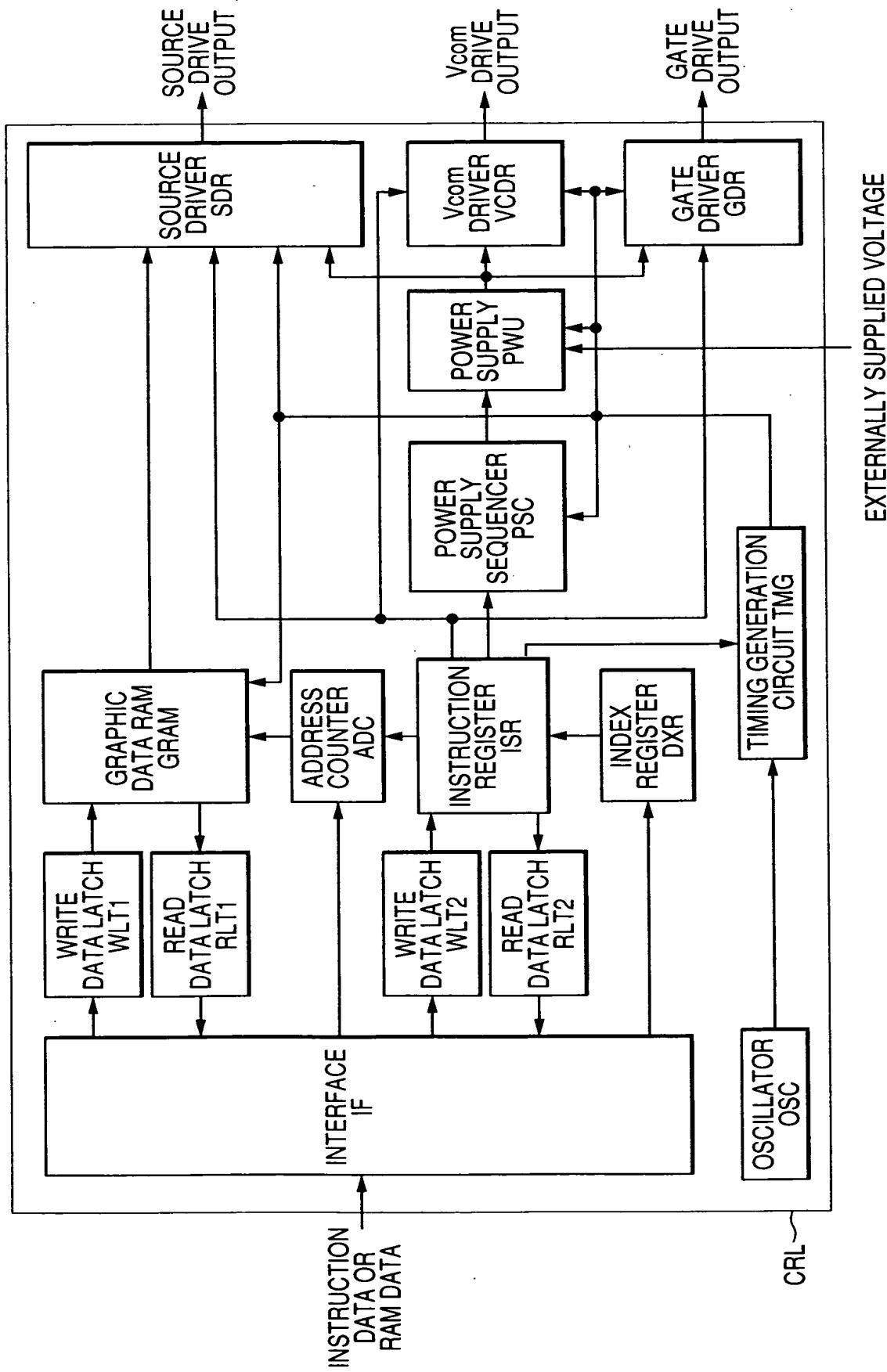


FIG. 5

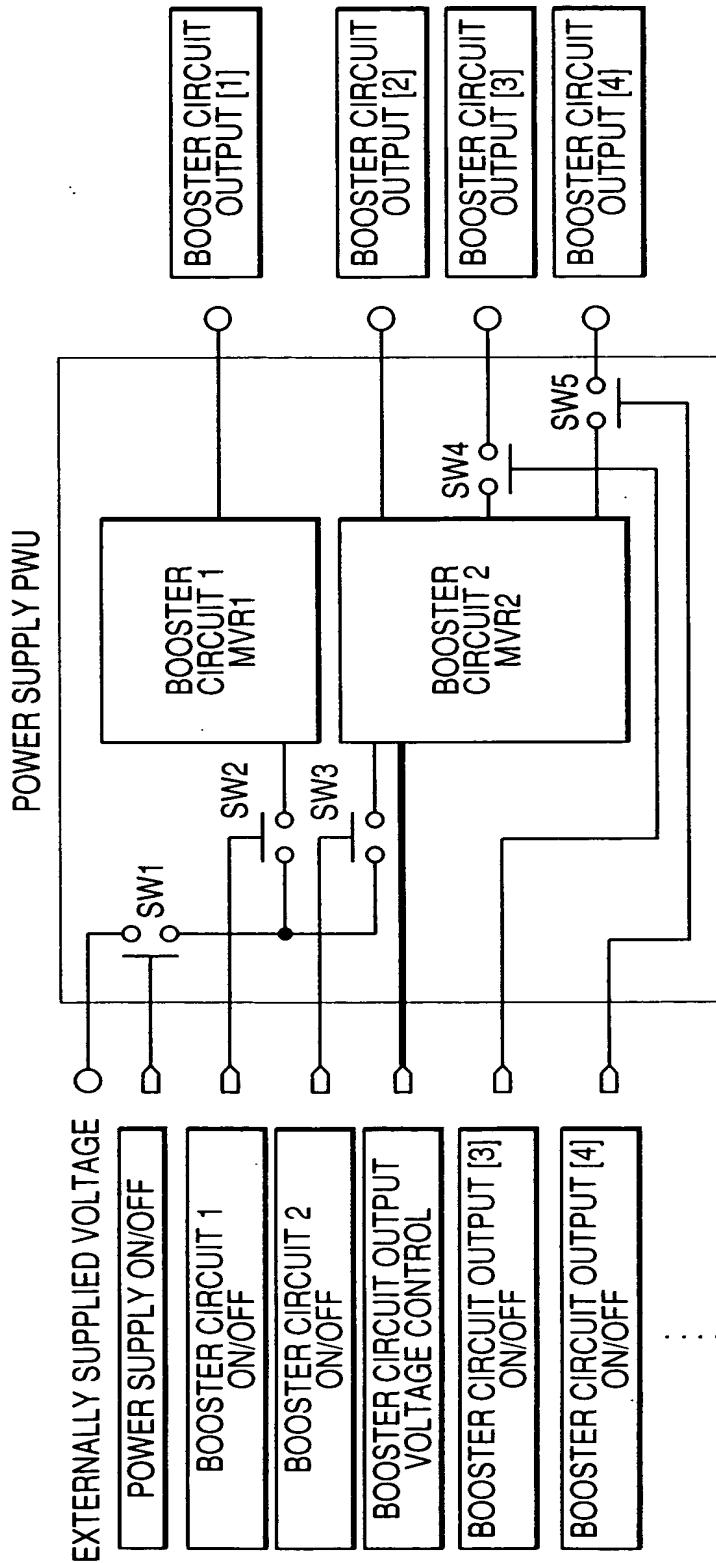


FIG. 6

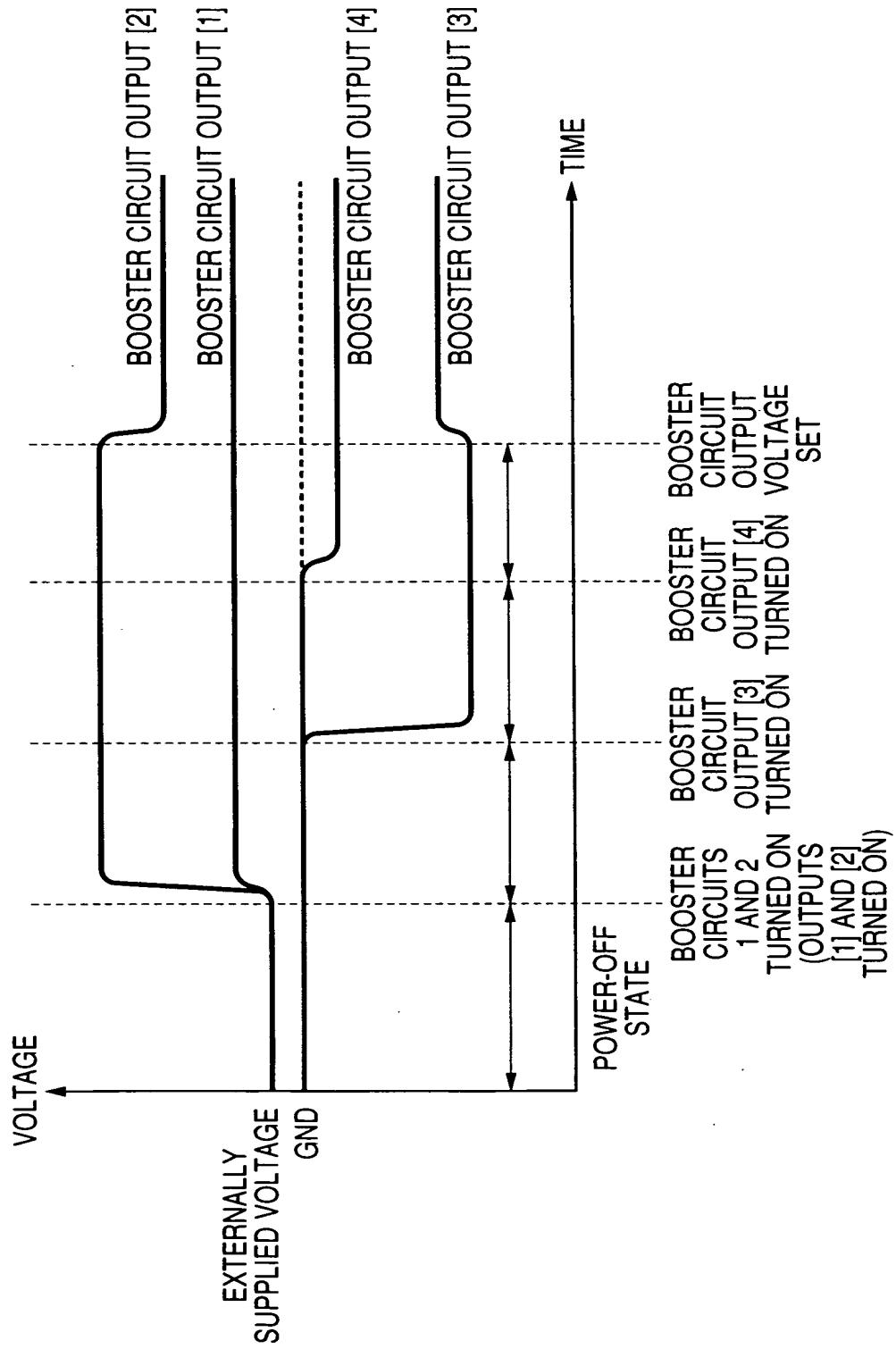


FIG. 7(a)
PANEL MANUFACTURED
BY X COMPANY

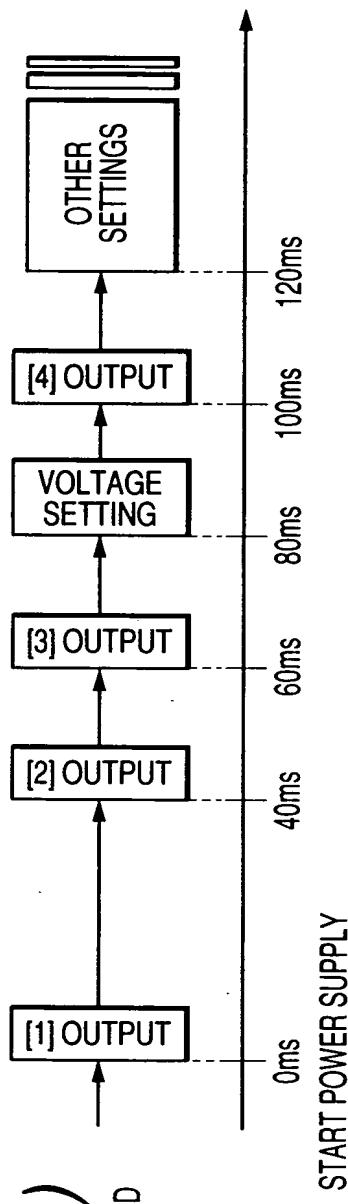


FIG. 7(b)
PANEL MANUFACTURED
BY Y COMPANY

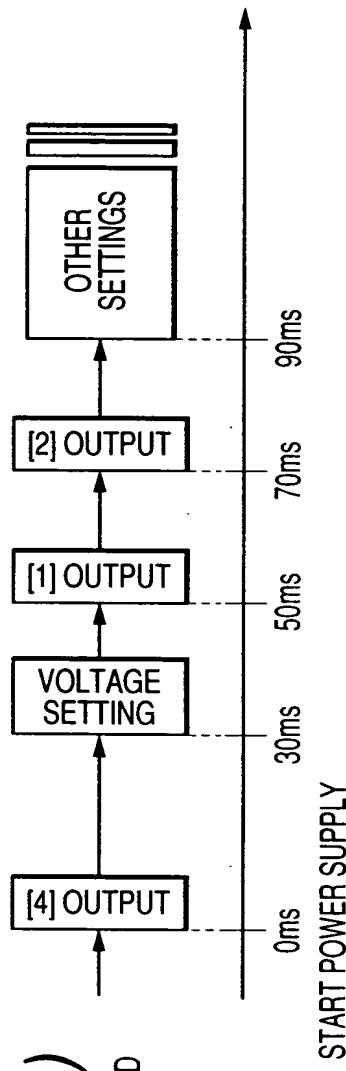
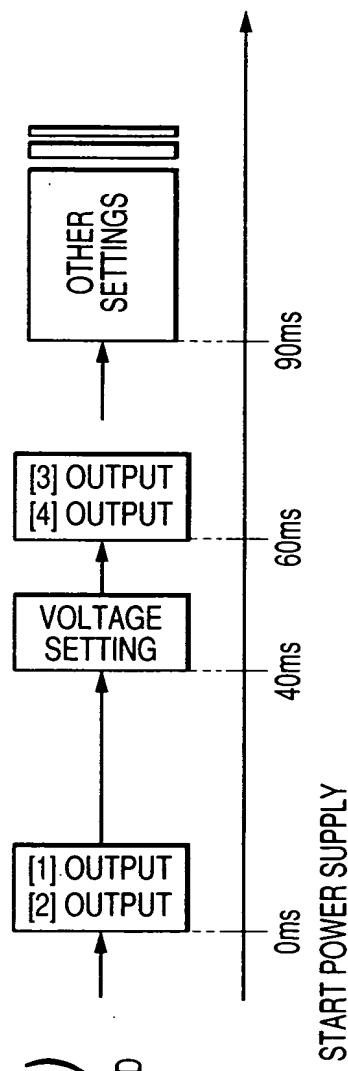


FIG. 7(c)
PANEL MANUFACTURED
BY Z COMPANY



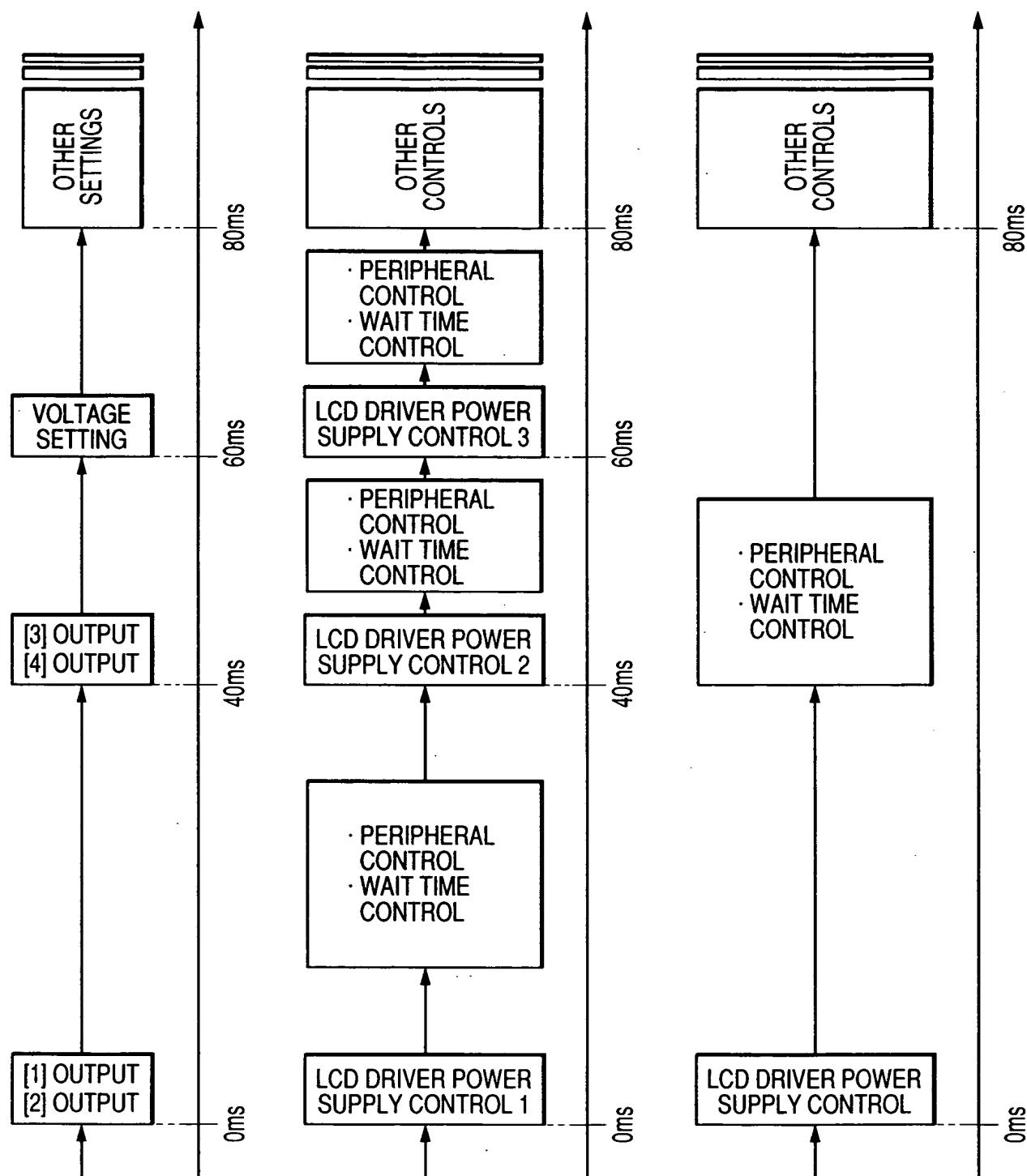


FIG. 8(a)
LIQUID CRYSTAL
DRIVER SETUP FLOW

FIG. 8(b)
MICROPROCESSOR
UNIT CONTROL
FLOW

FIG. 9

FIG. 10

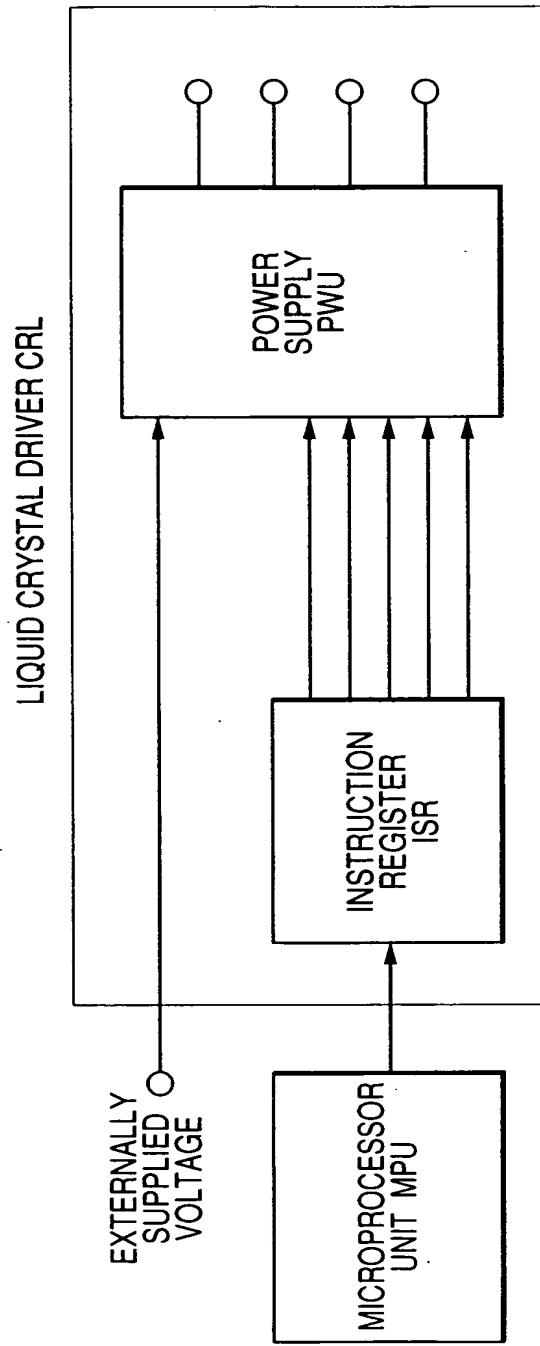


FIG. 11

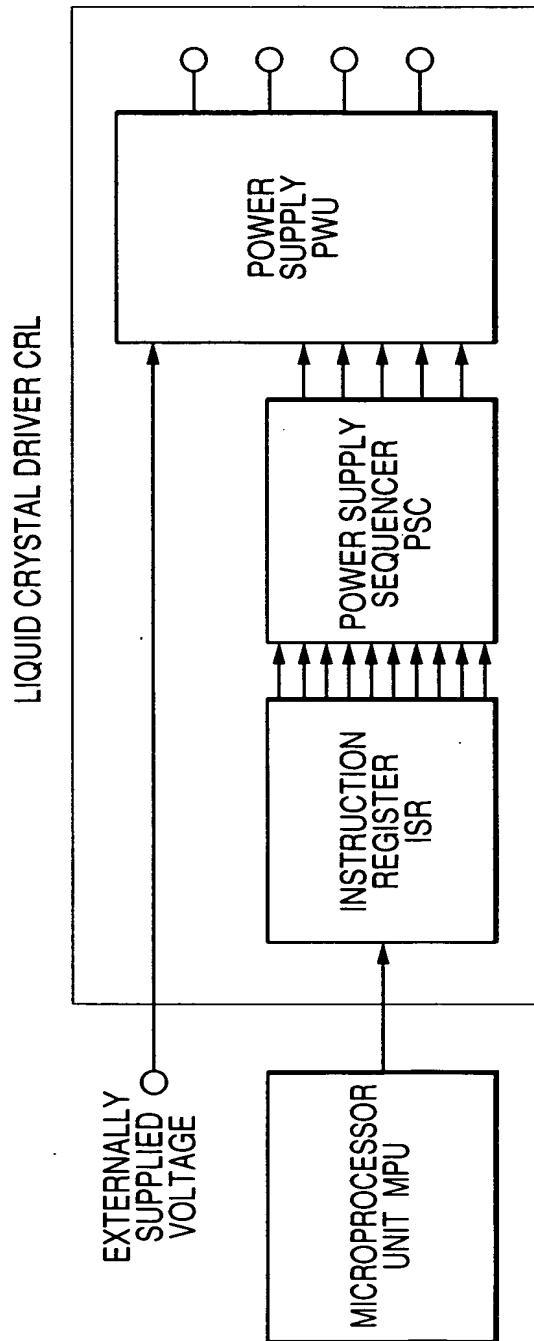


FIG. 12

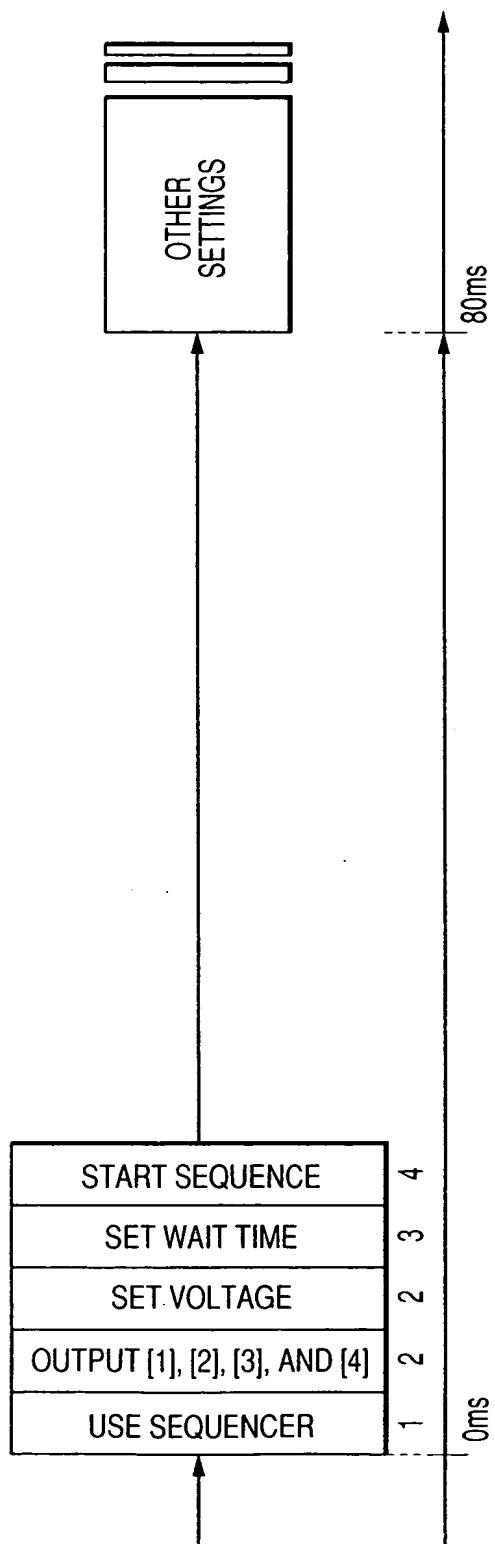


FIG. 13

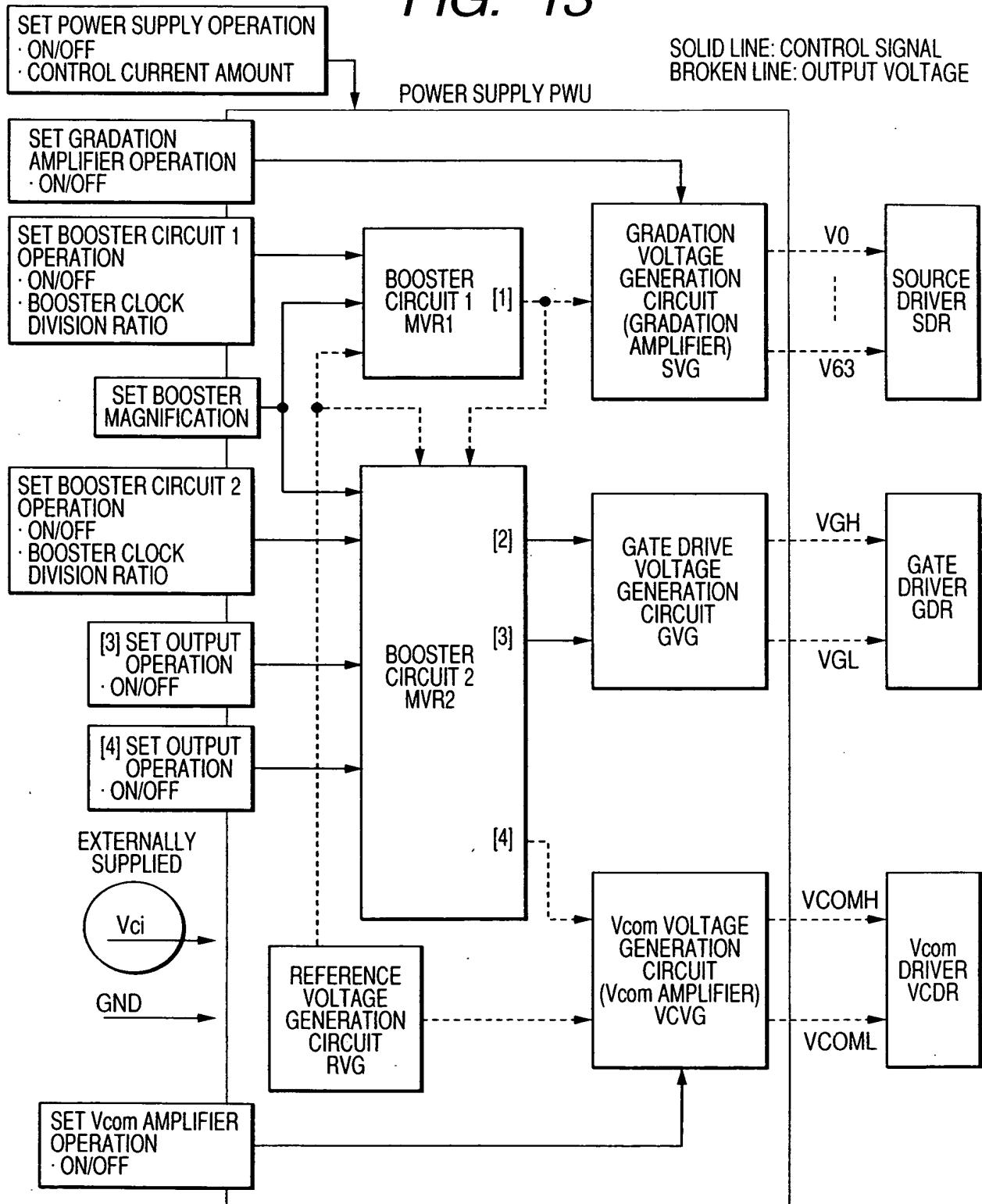


FIG. 14(a)

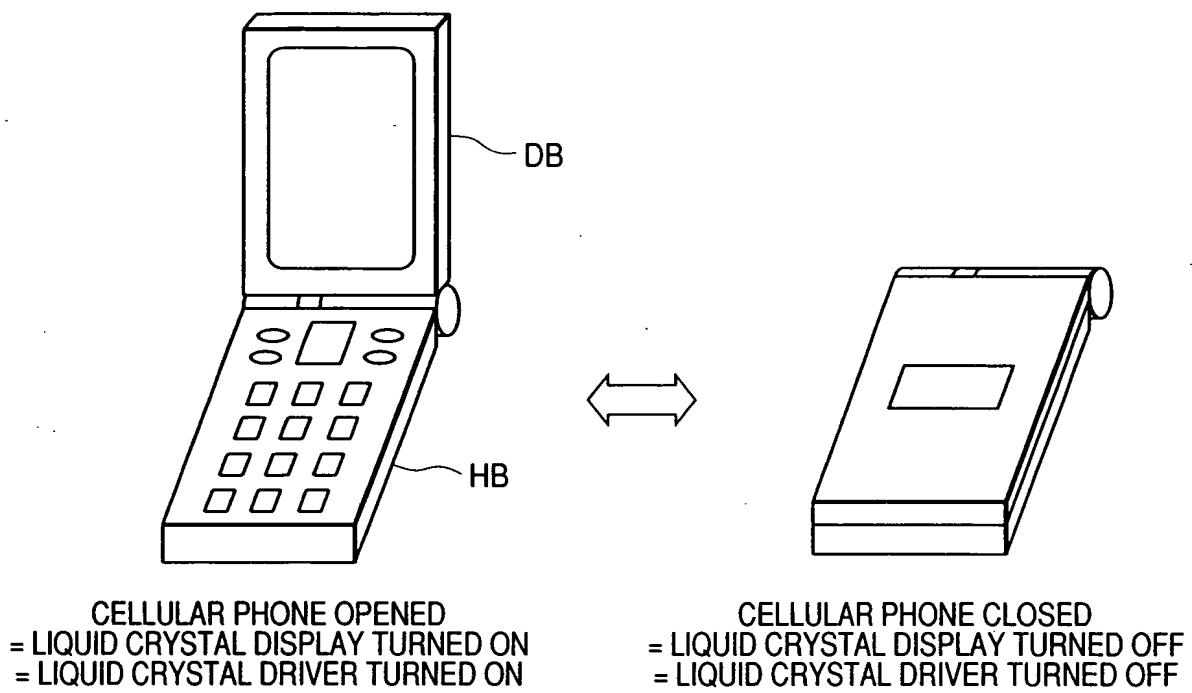


FIG. 14(b)

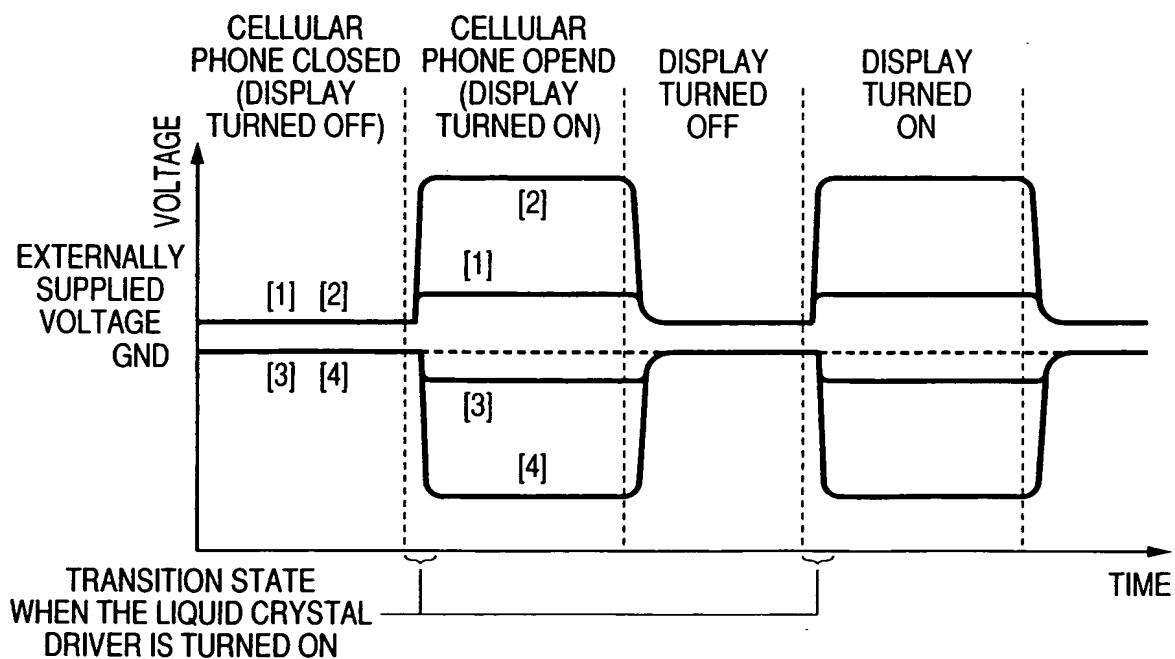


FIG. 15

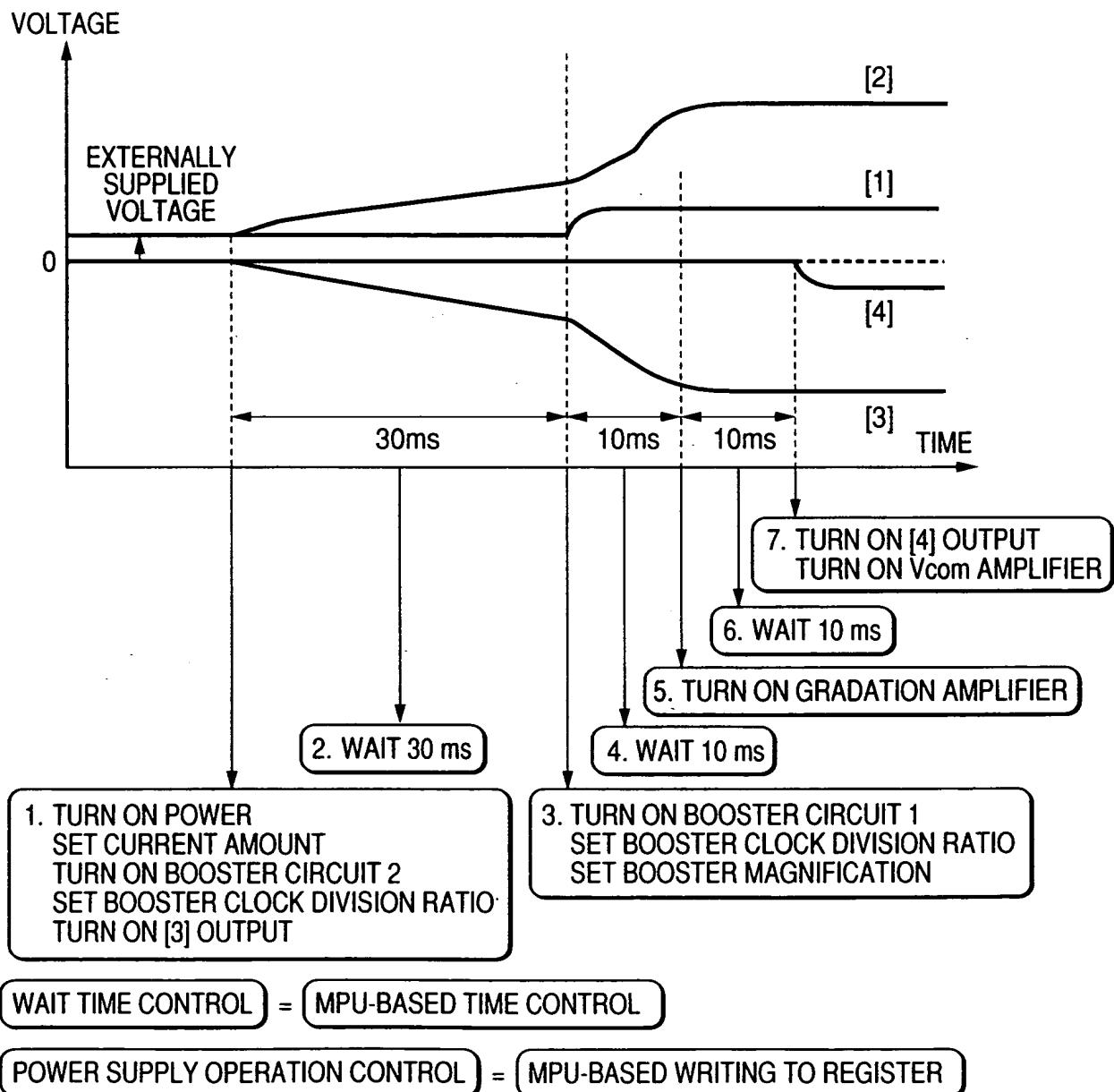


FIG. 16

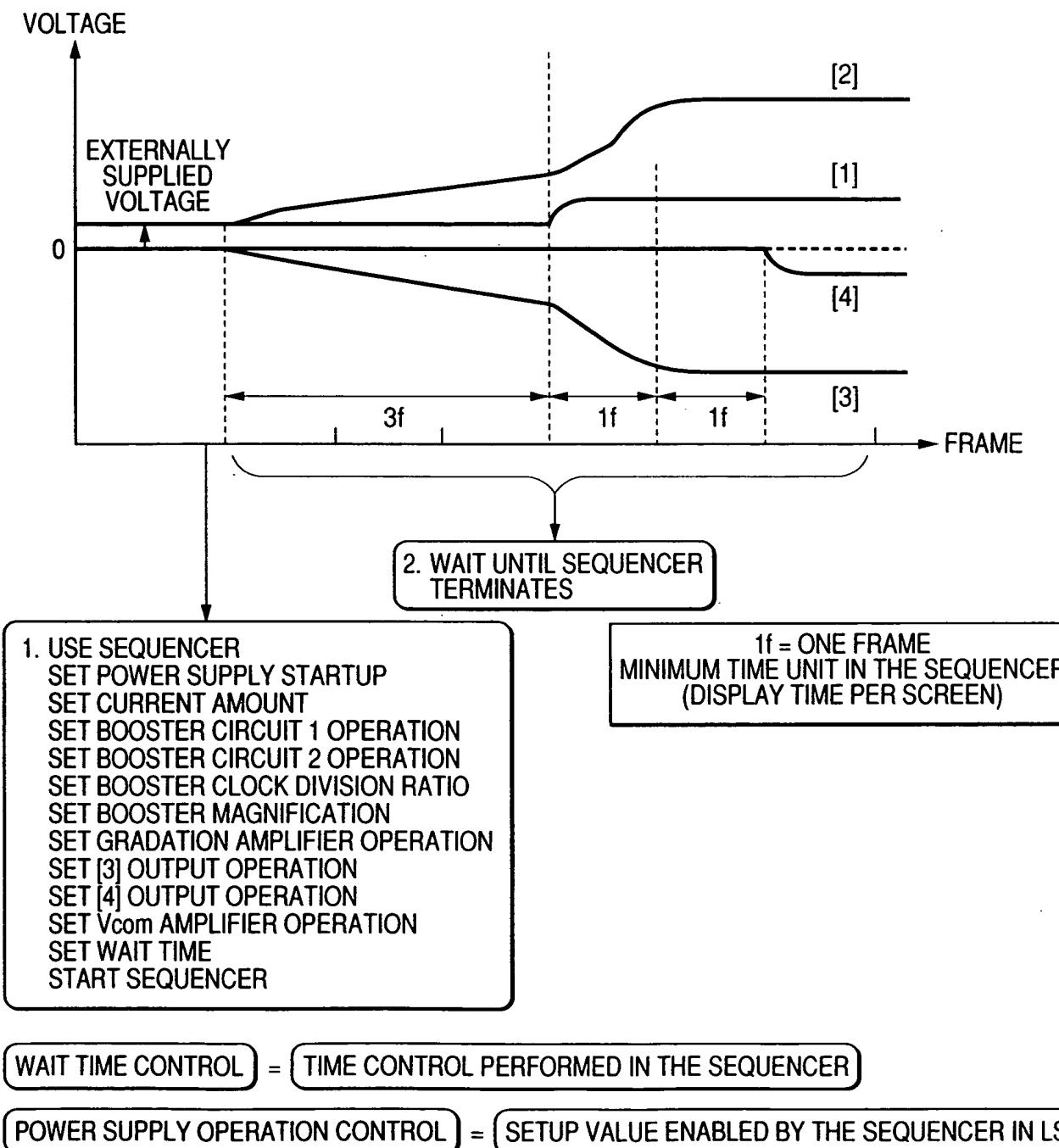


FIG. 17

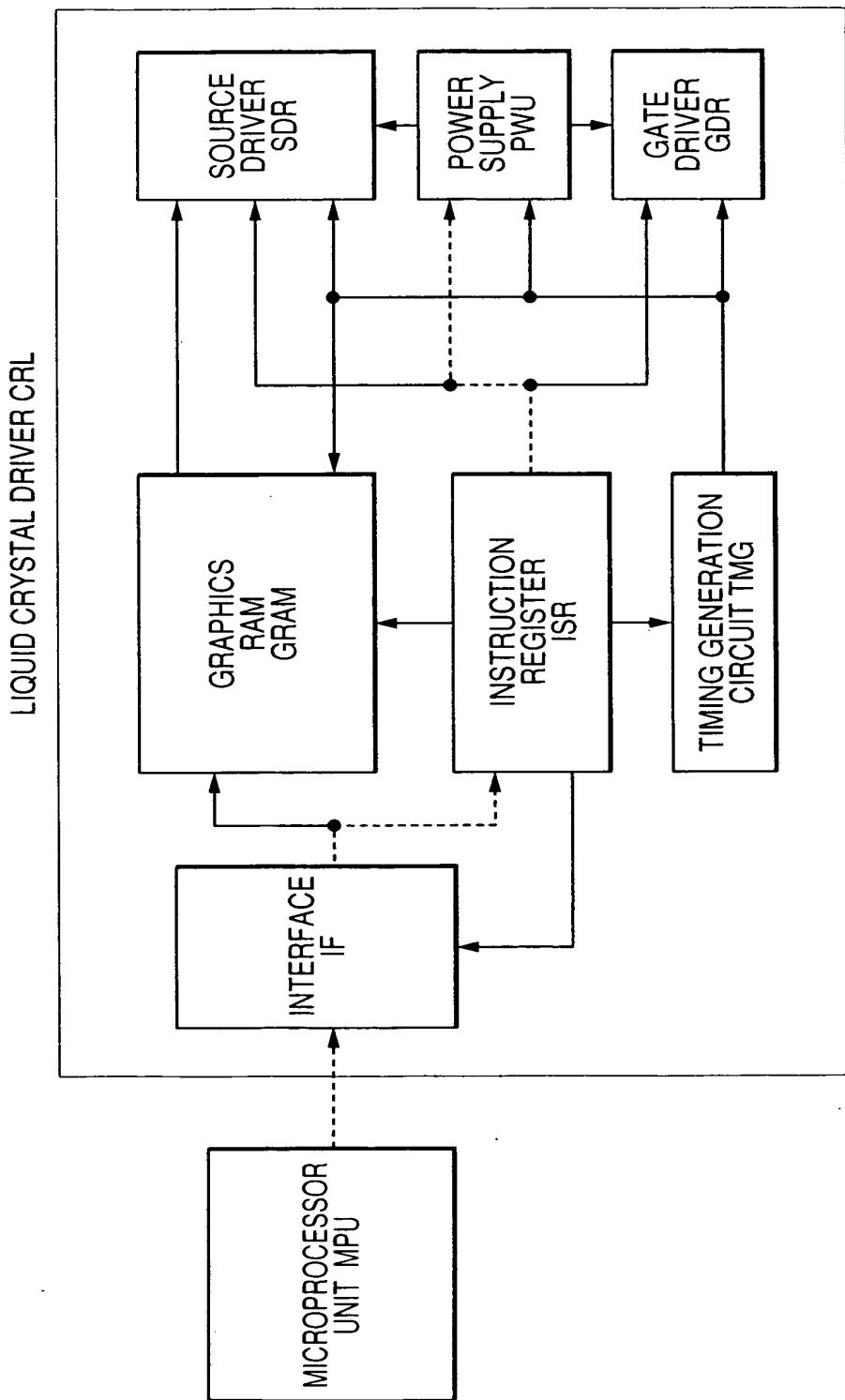


FIG. 18

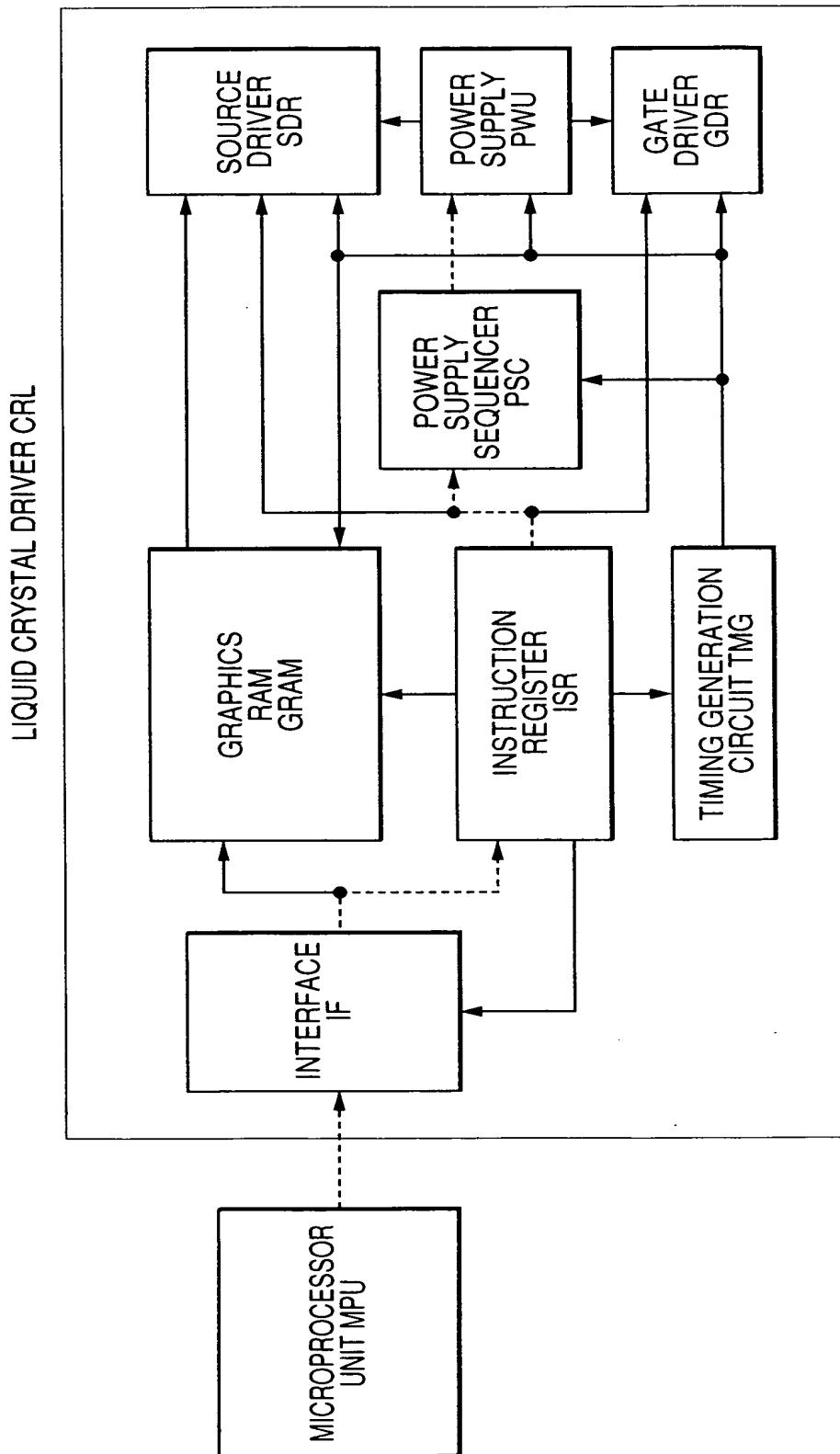


FIG. 19

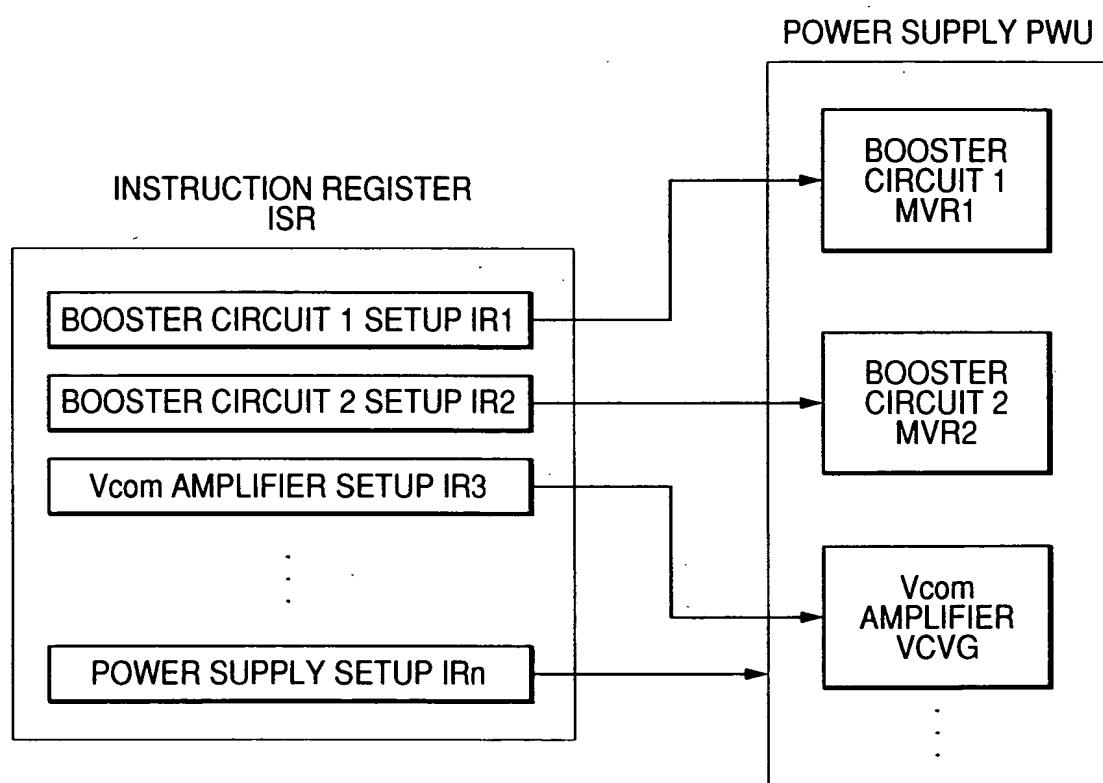


FIG. 20

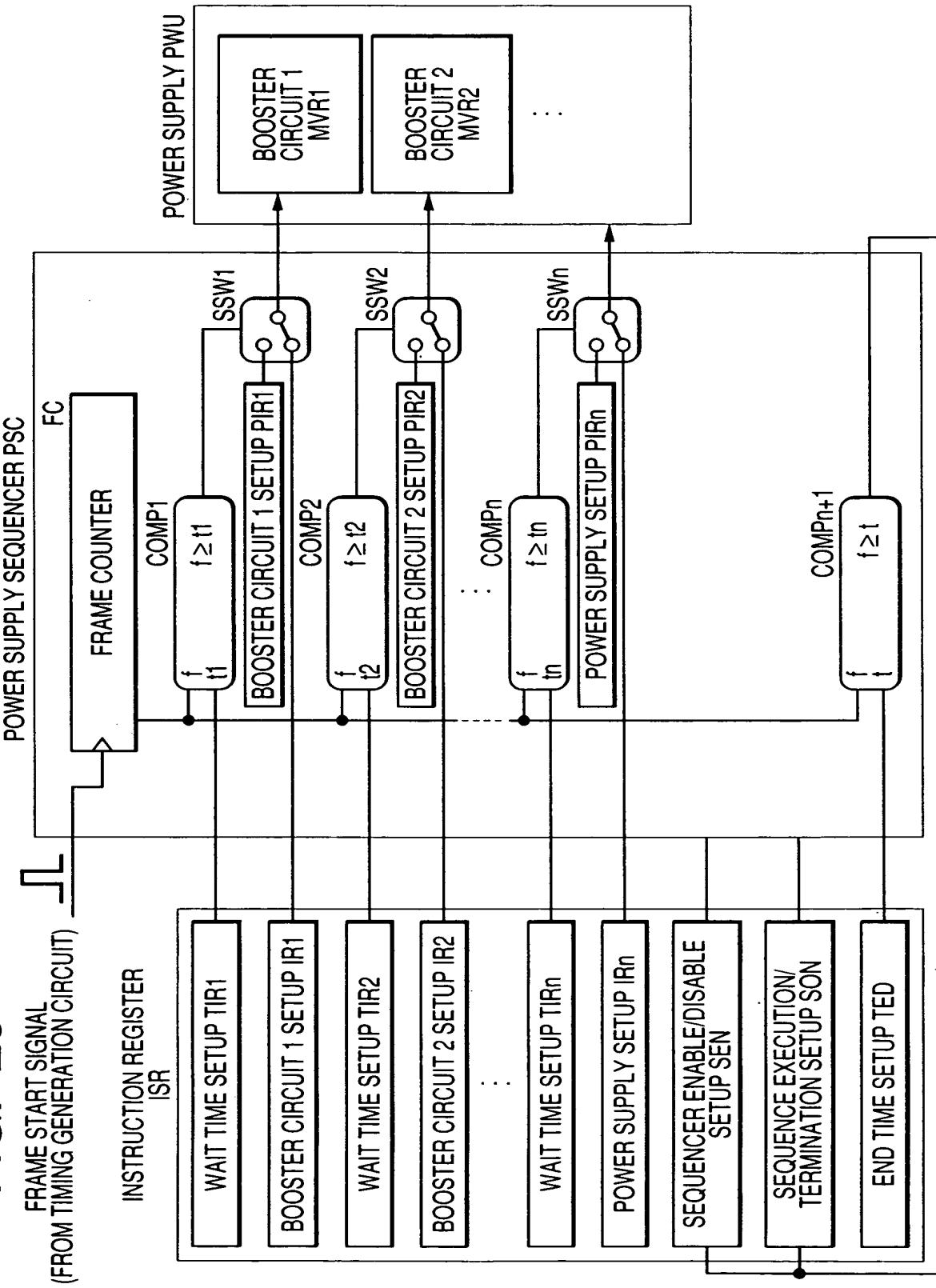


FIG. 21(a) FIG. 21(b) FIG. 21(c)

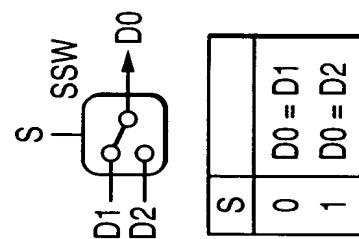
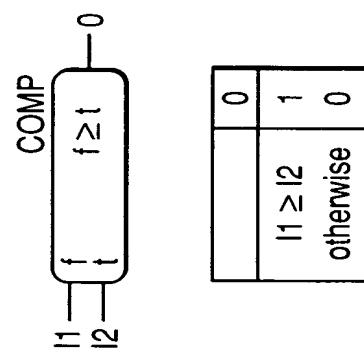
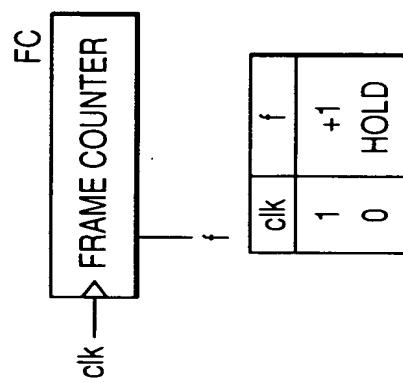


FIG. 22

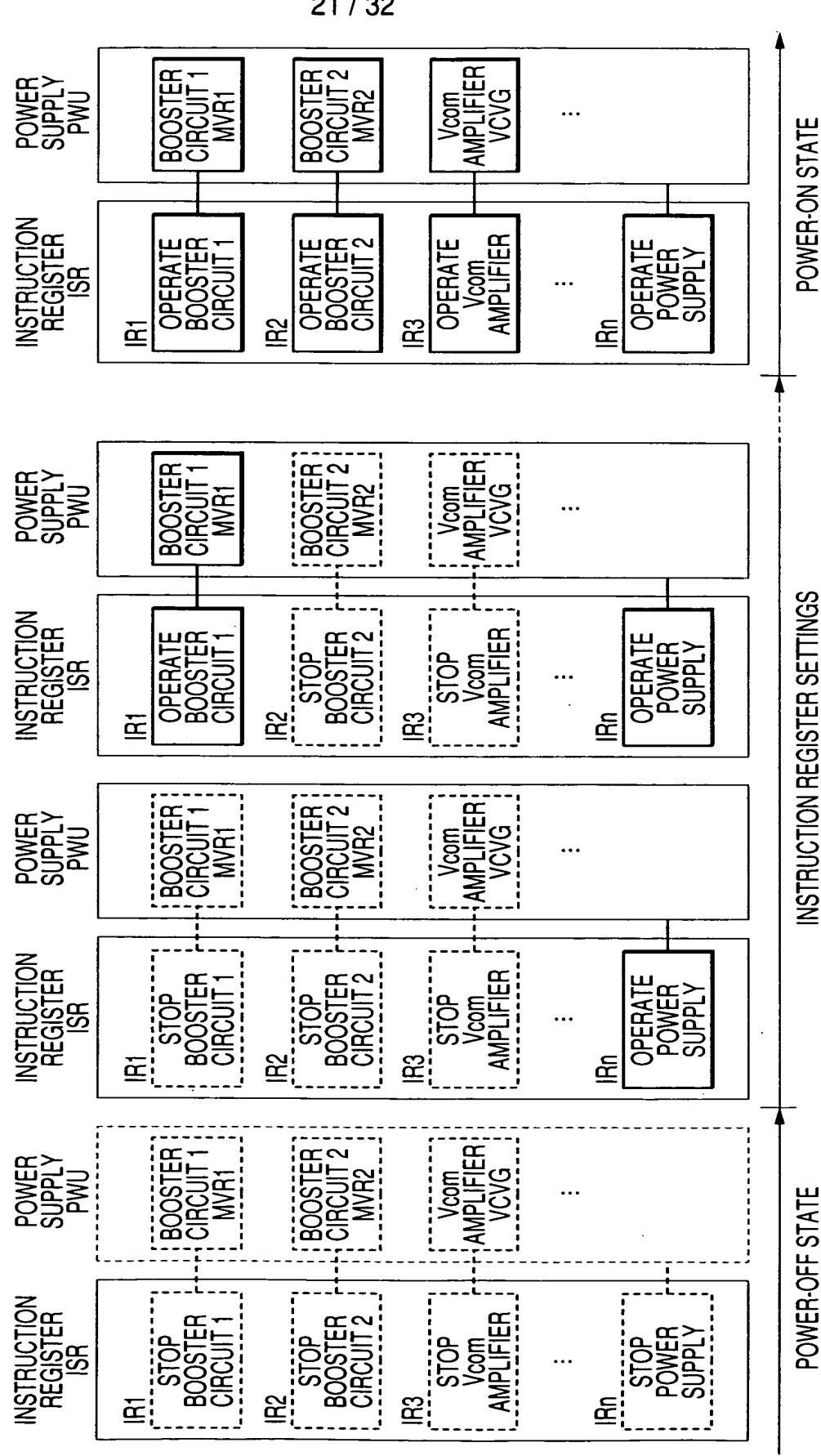


FIG. 23

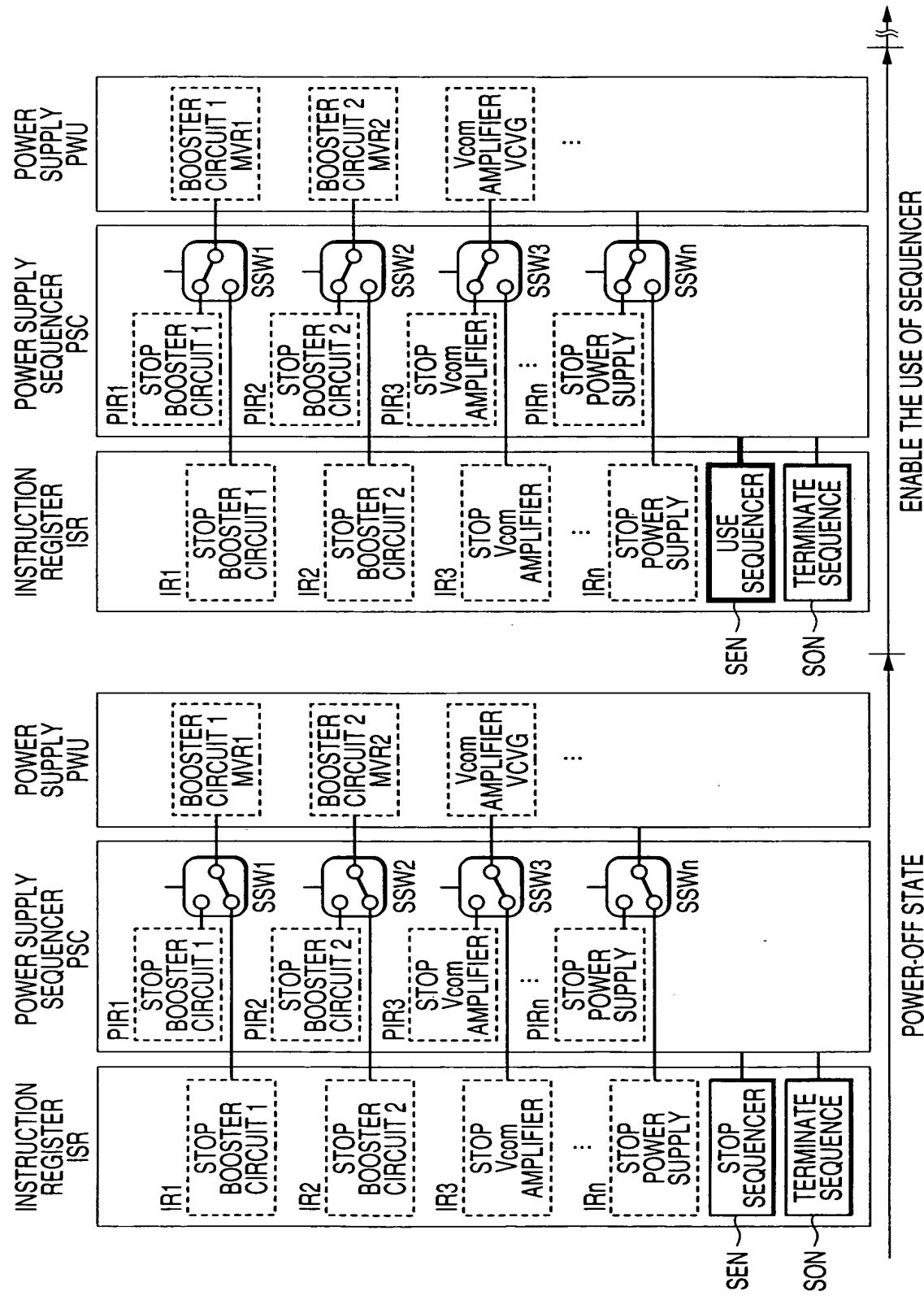


FIG. 24

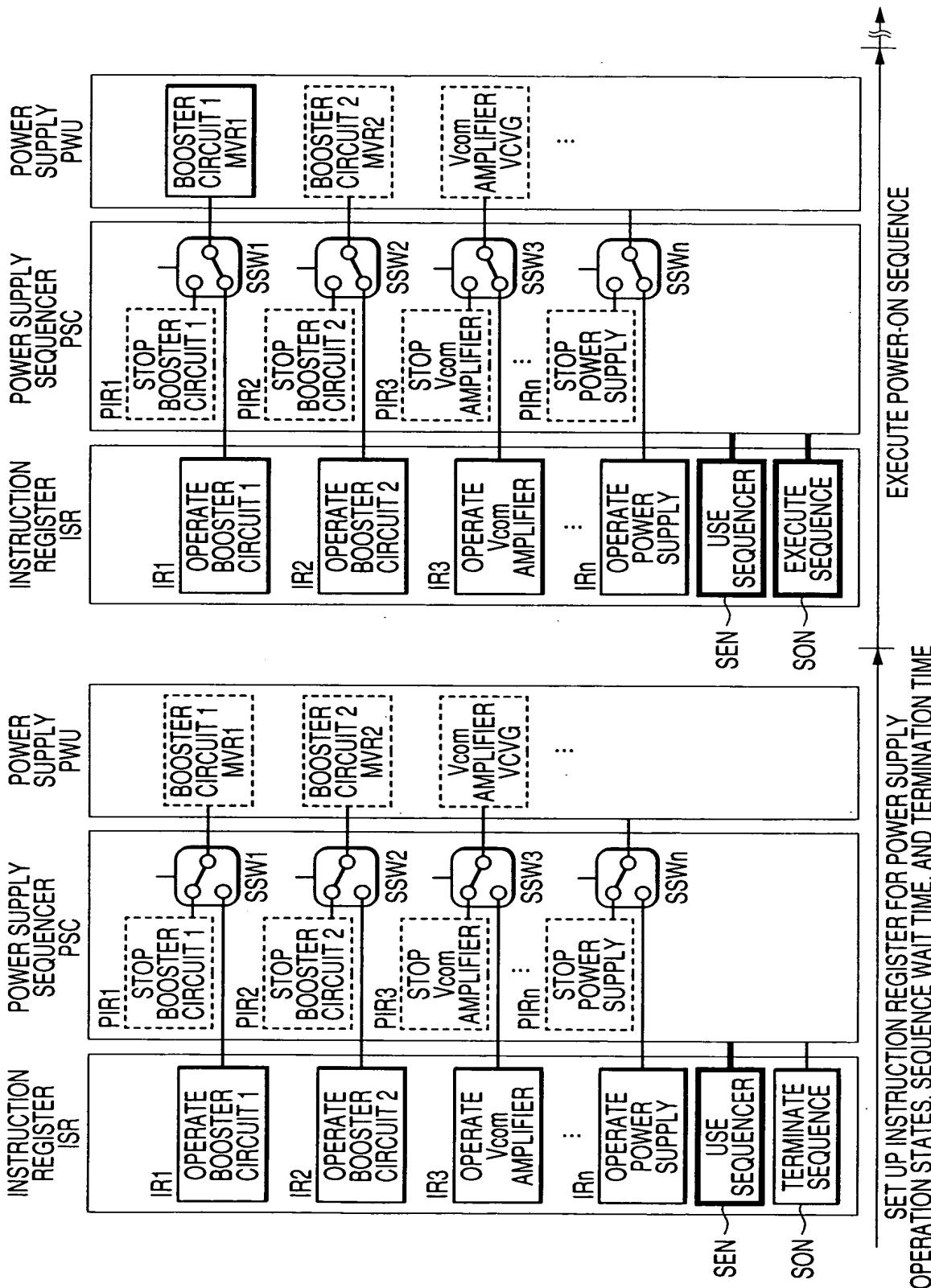


FIG. 25

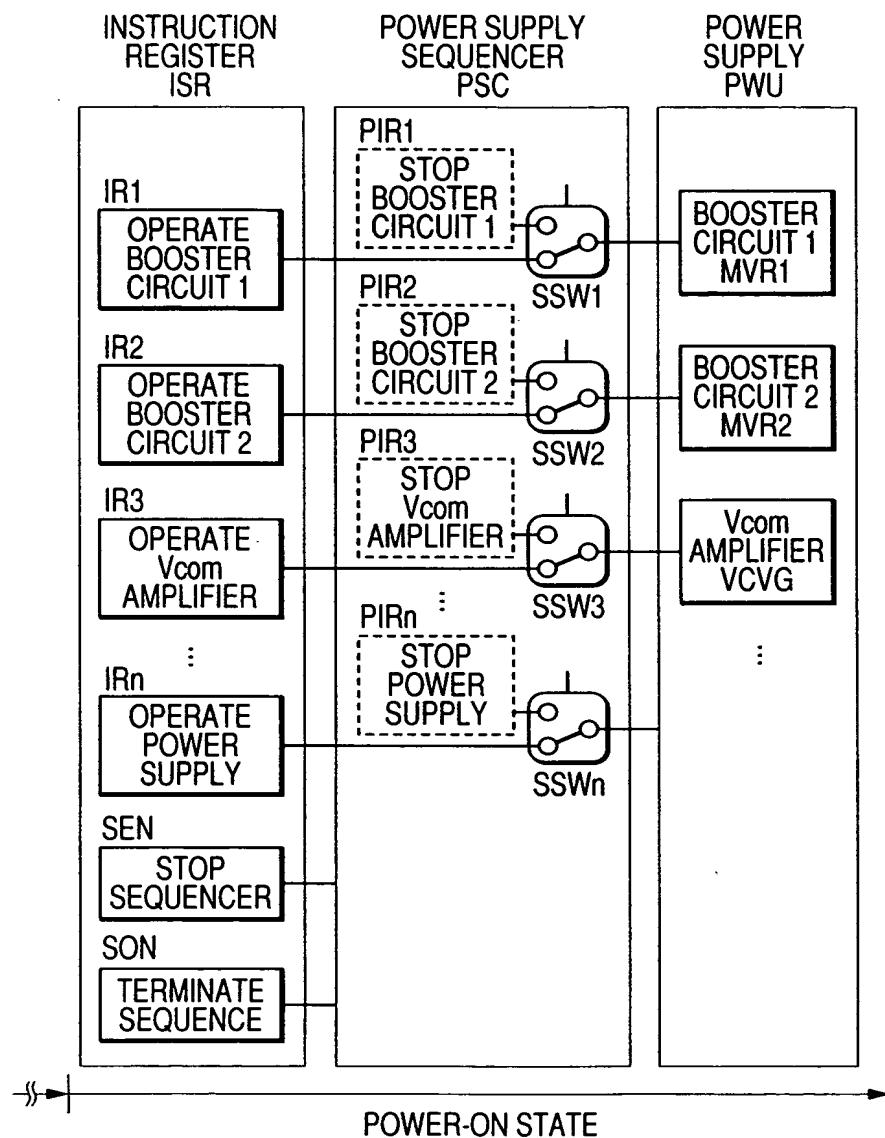


FIG. 26

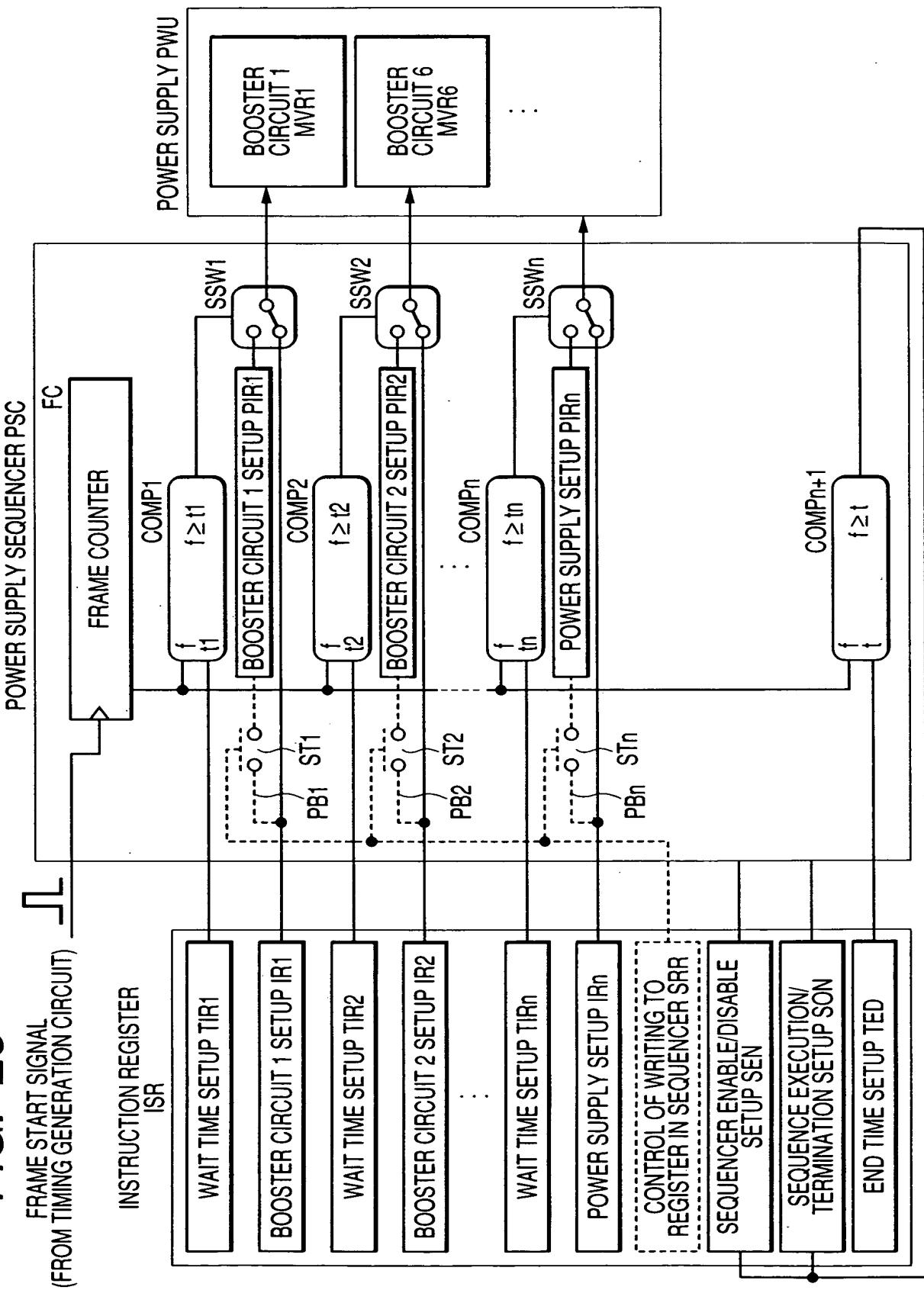


FIG. 27

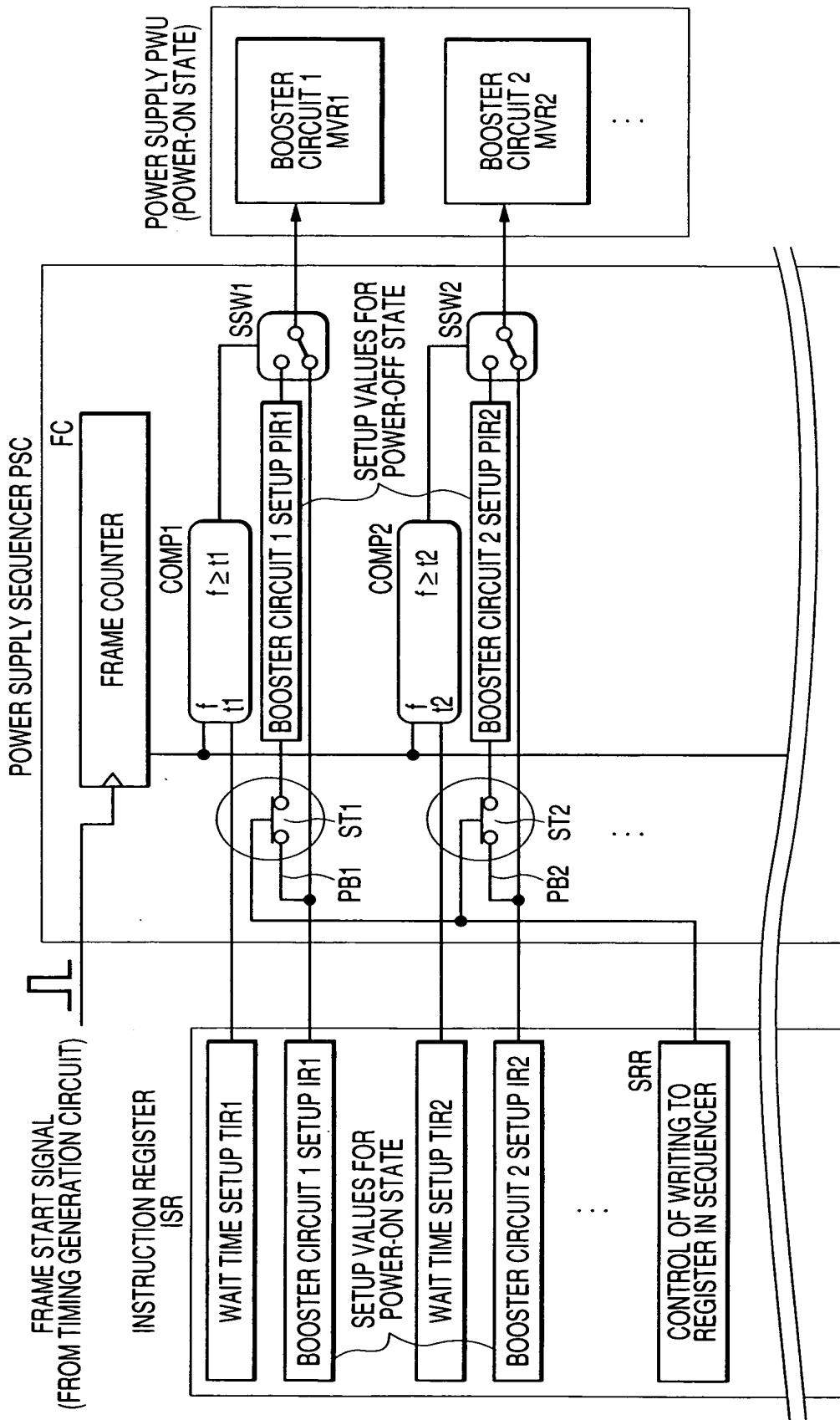


FIG. 28

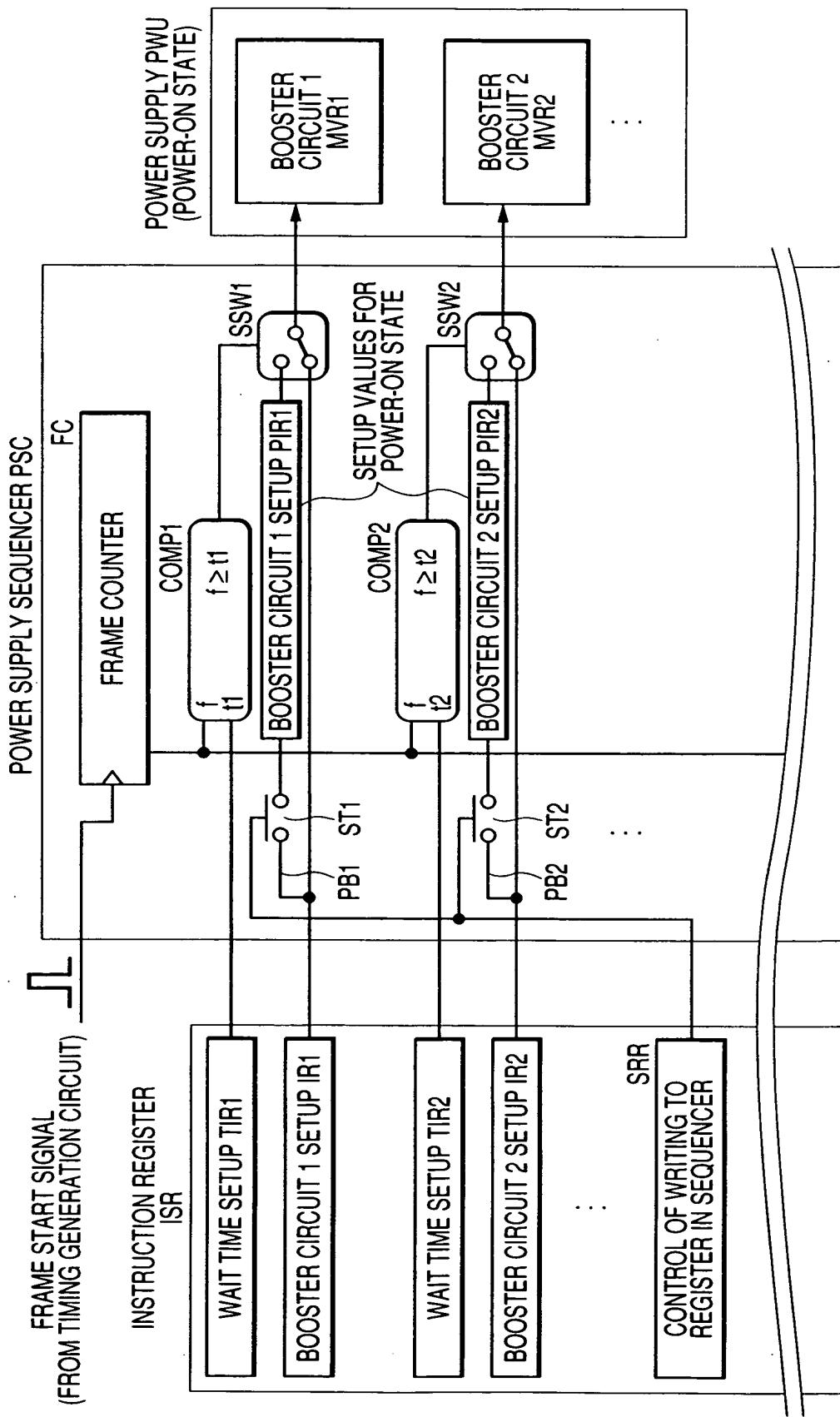


FIG. 29

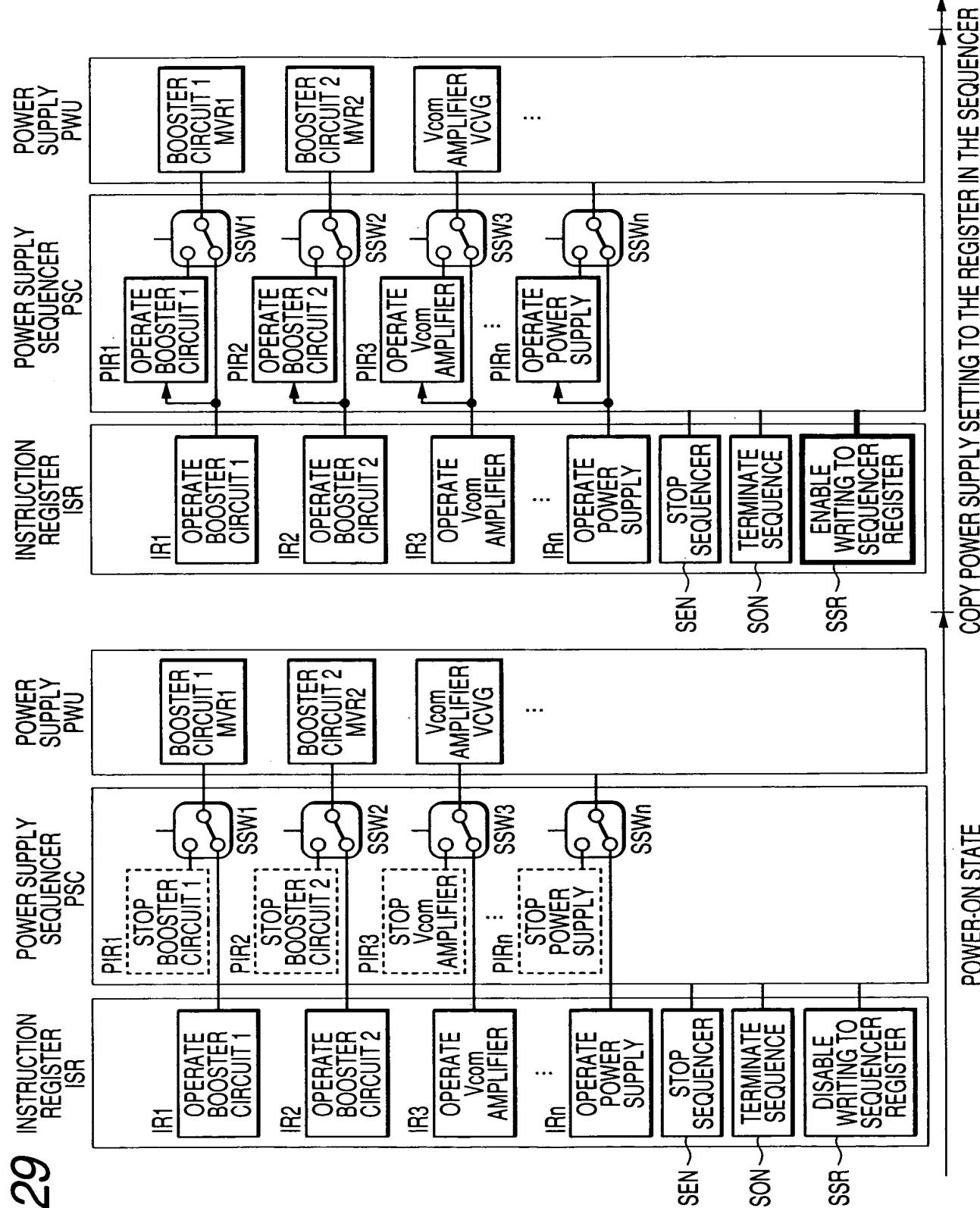


FIG. 30

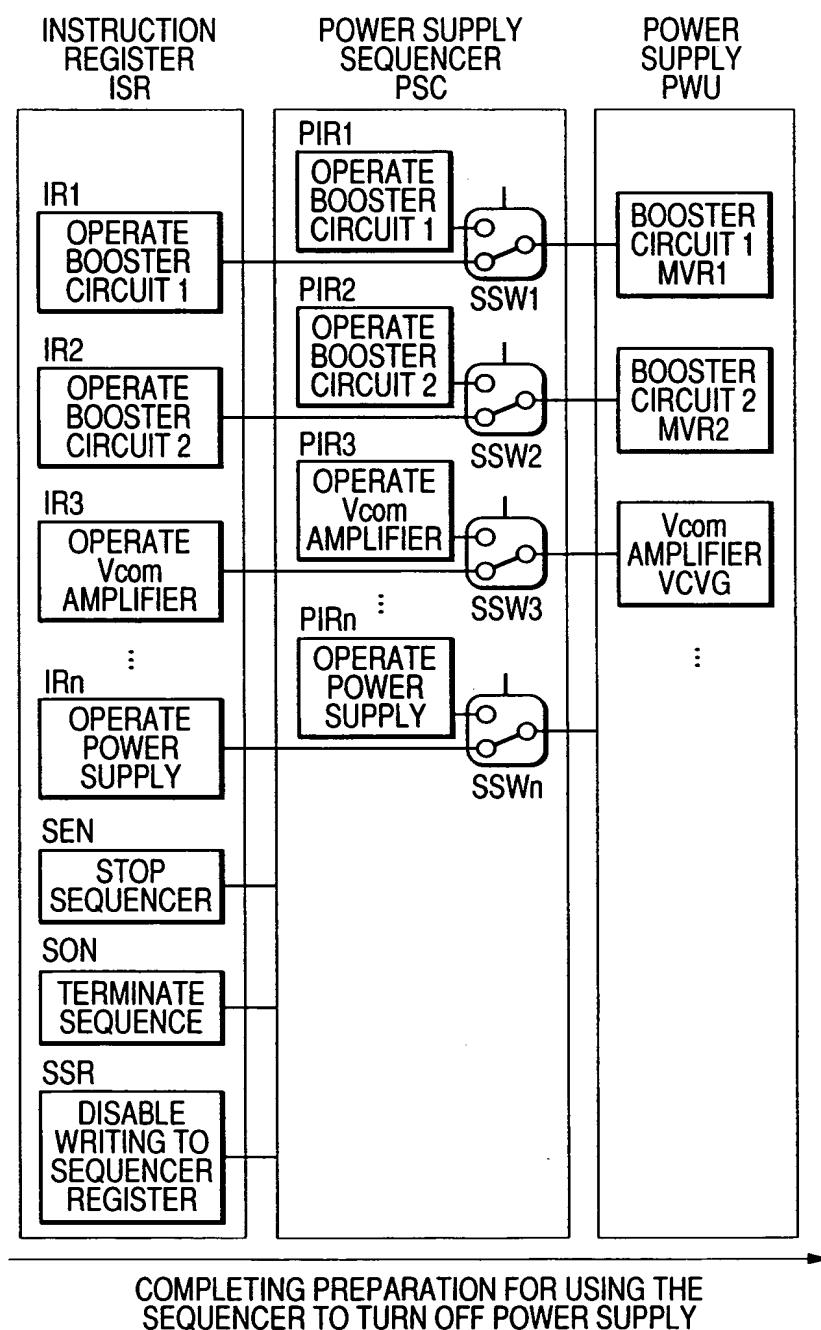


FIG. 31

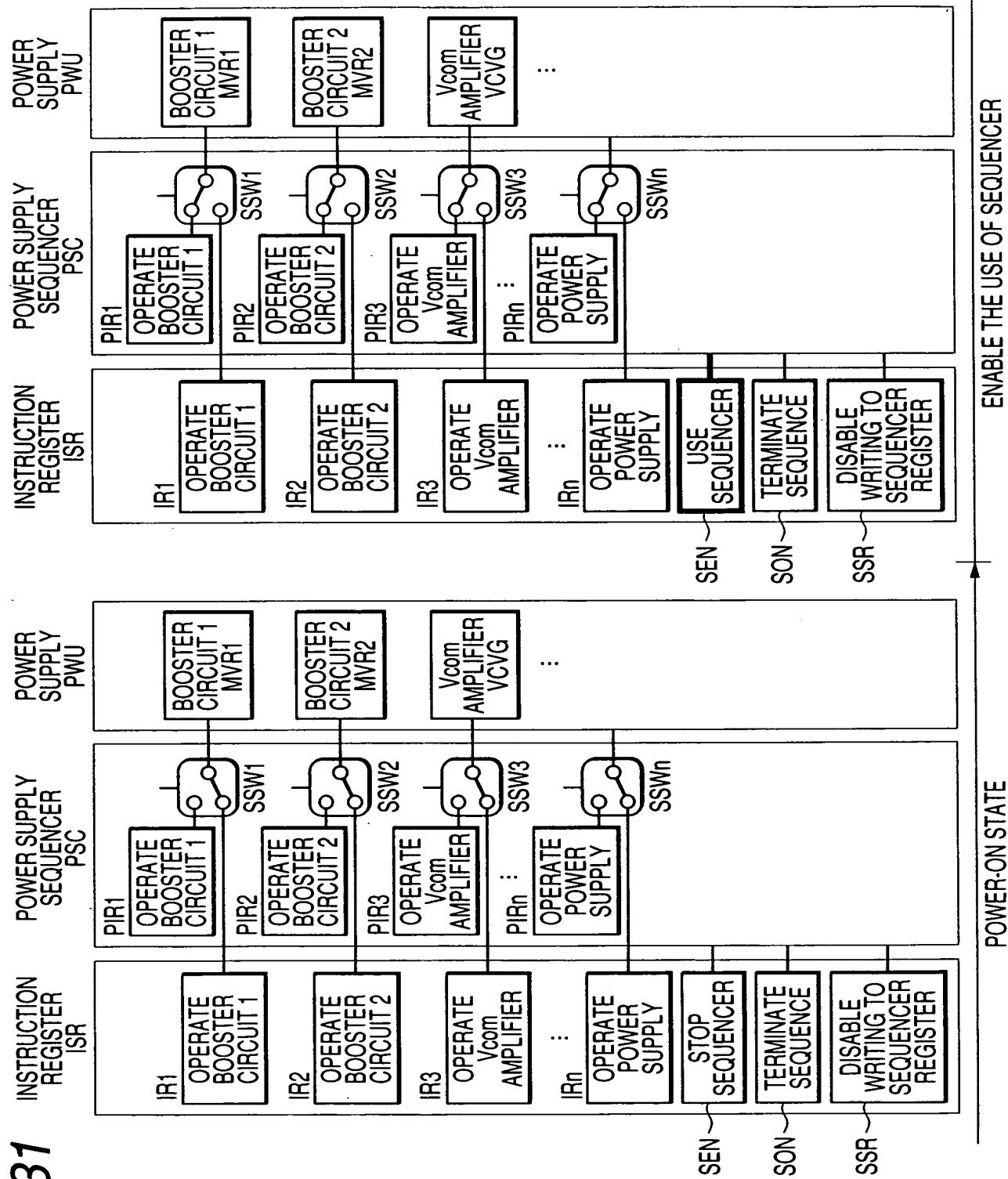


FIG. 32

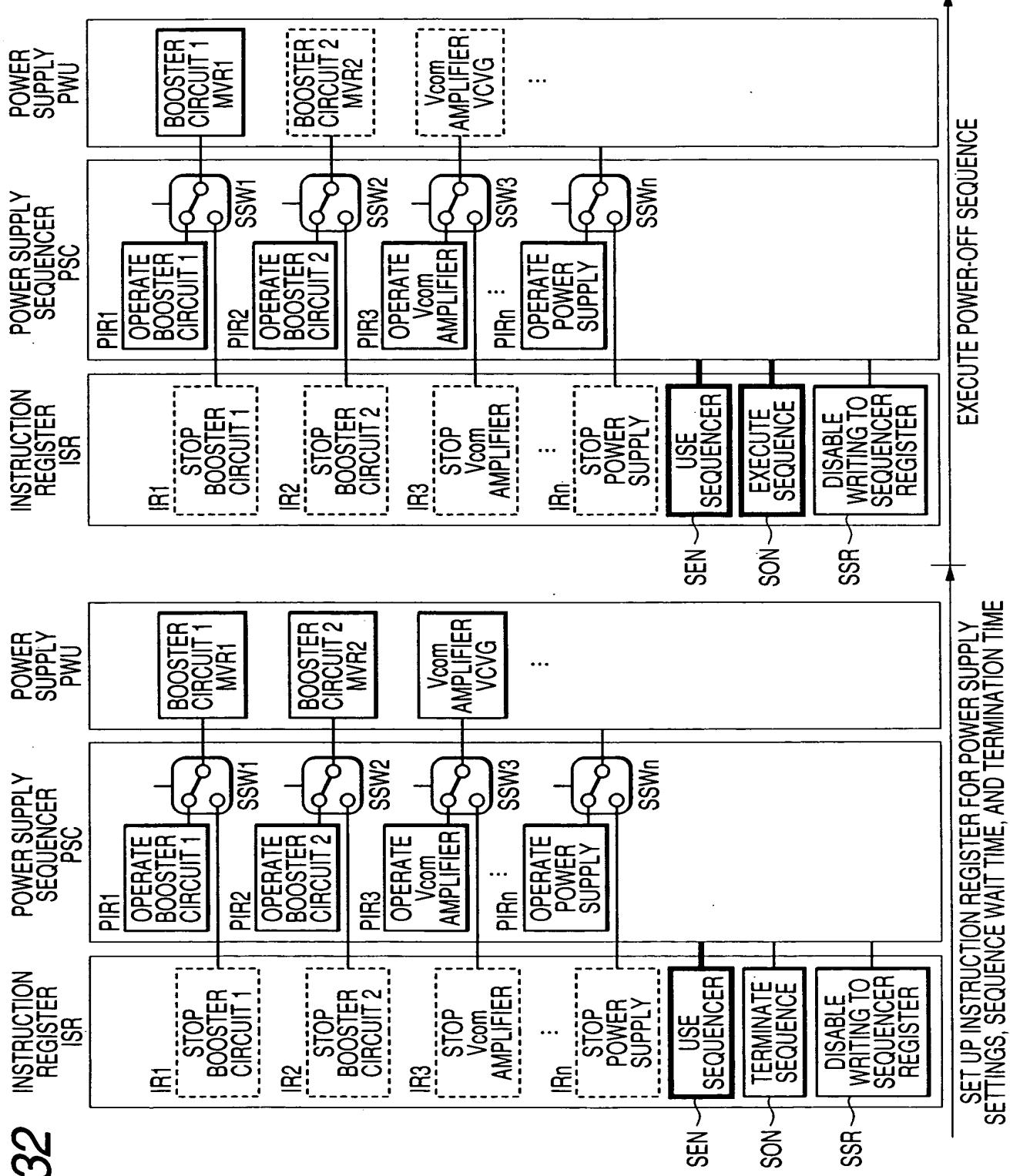


FIG. 33

